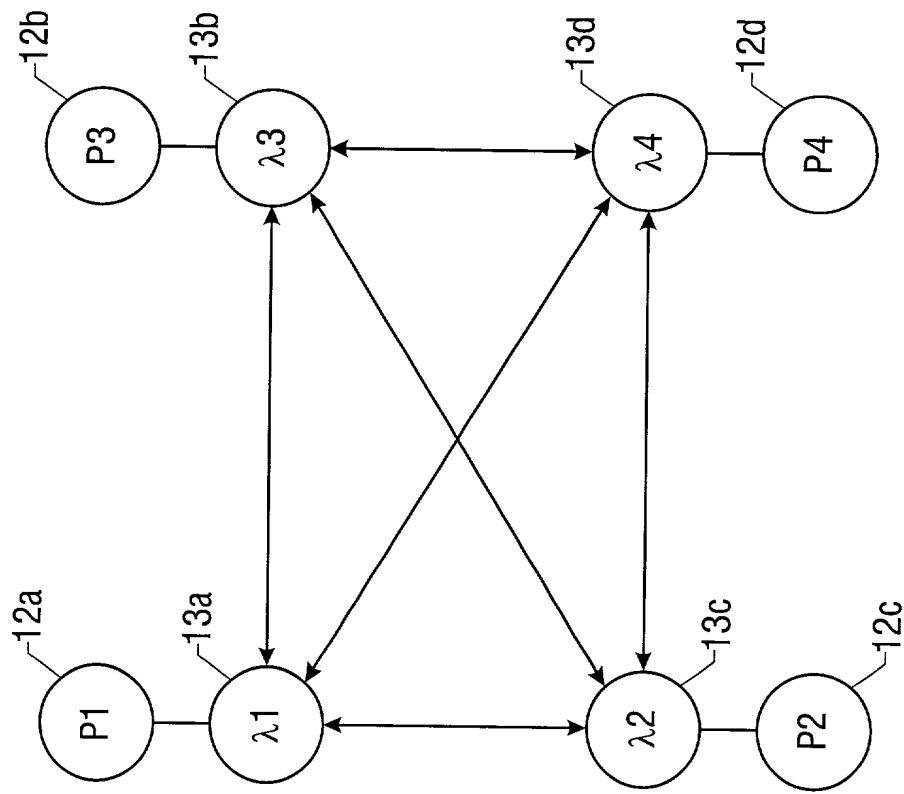


FIG. 1 is a schematic diagram of a network topology. The network includes four nodes labeled P1, P2, P3, and P4, and four nodes labeled λ1, λ2, λ3, and λ4. Node P1 is connected to node λ1 by a line labeled 12a. Node P2 is connected to node λ2 by a line labeled 12c. Node P3 is connected to node λ3 by a line labeled 12b. Node P4 is connected to node λ4 by a line labeled 12d. Node λ1 is connected to node λ2 by a line labeled 13a. Node λ2 is connected to node λ3 by a line labeled 13c. Node λ3 is connected to node λ4 by a line labeled 13d. Node λ1 is also connected to node λ4 by a line labeled 13b. Node λ2 is also connected to node λ3 by a line labeled 13a. The network is a fully connected mesh topology.



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FIG. 1

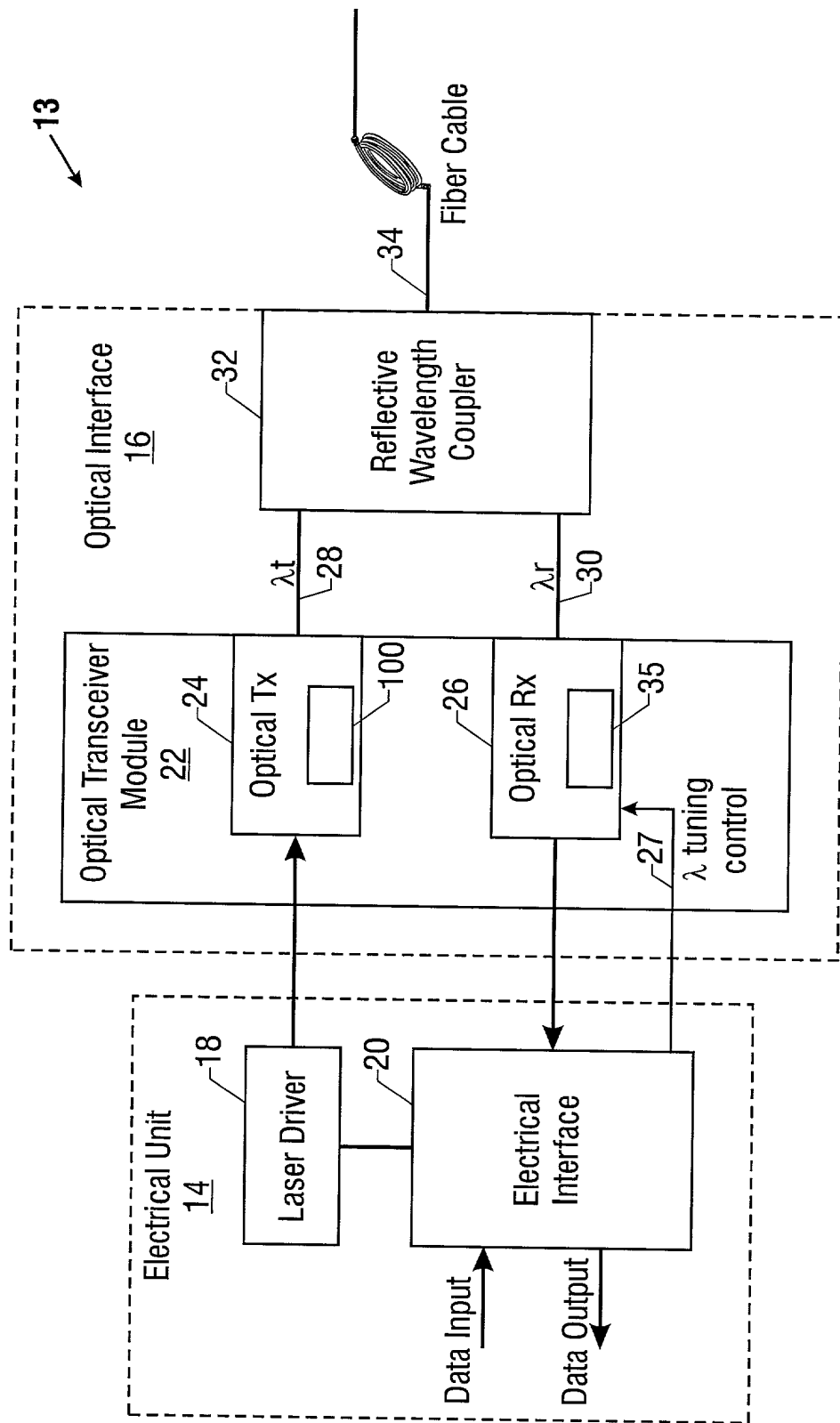


FIG. 2

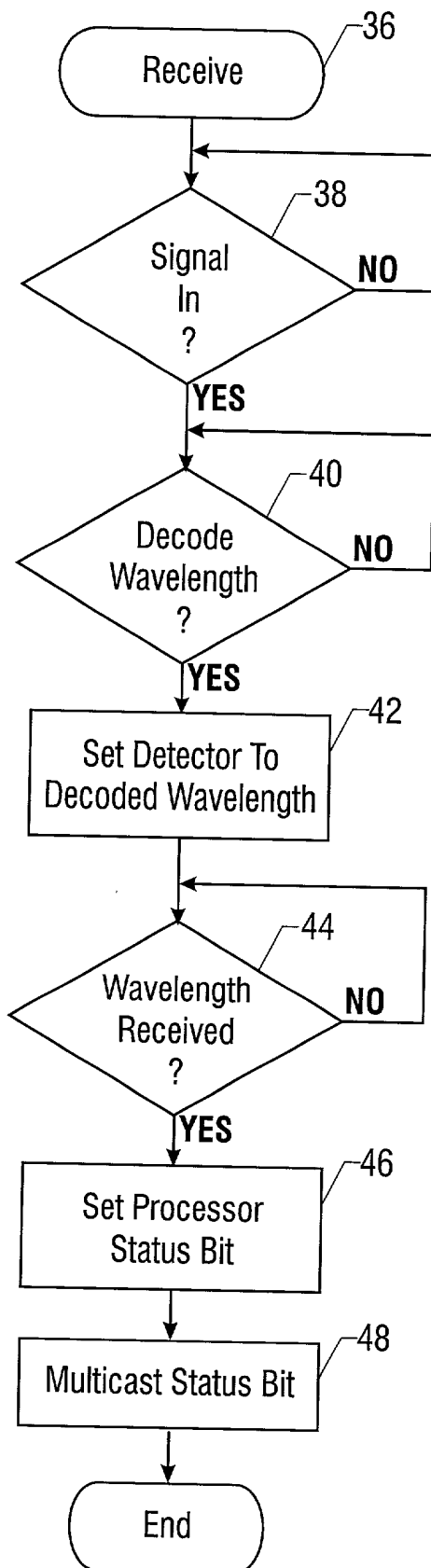
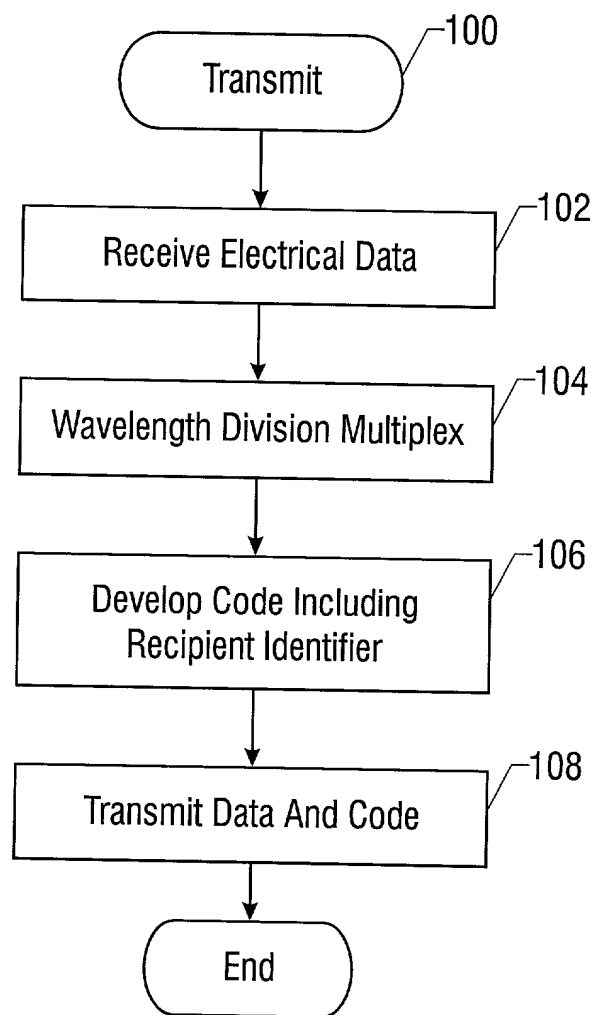


FIG. 3A



**FIG. 3B**

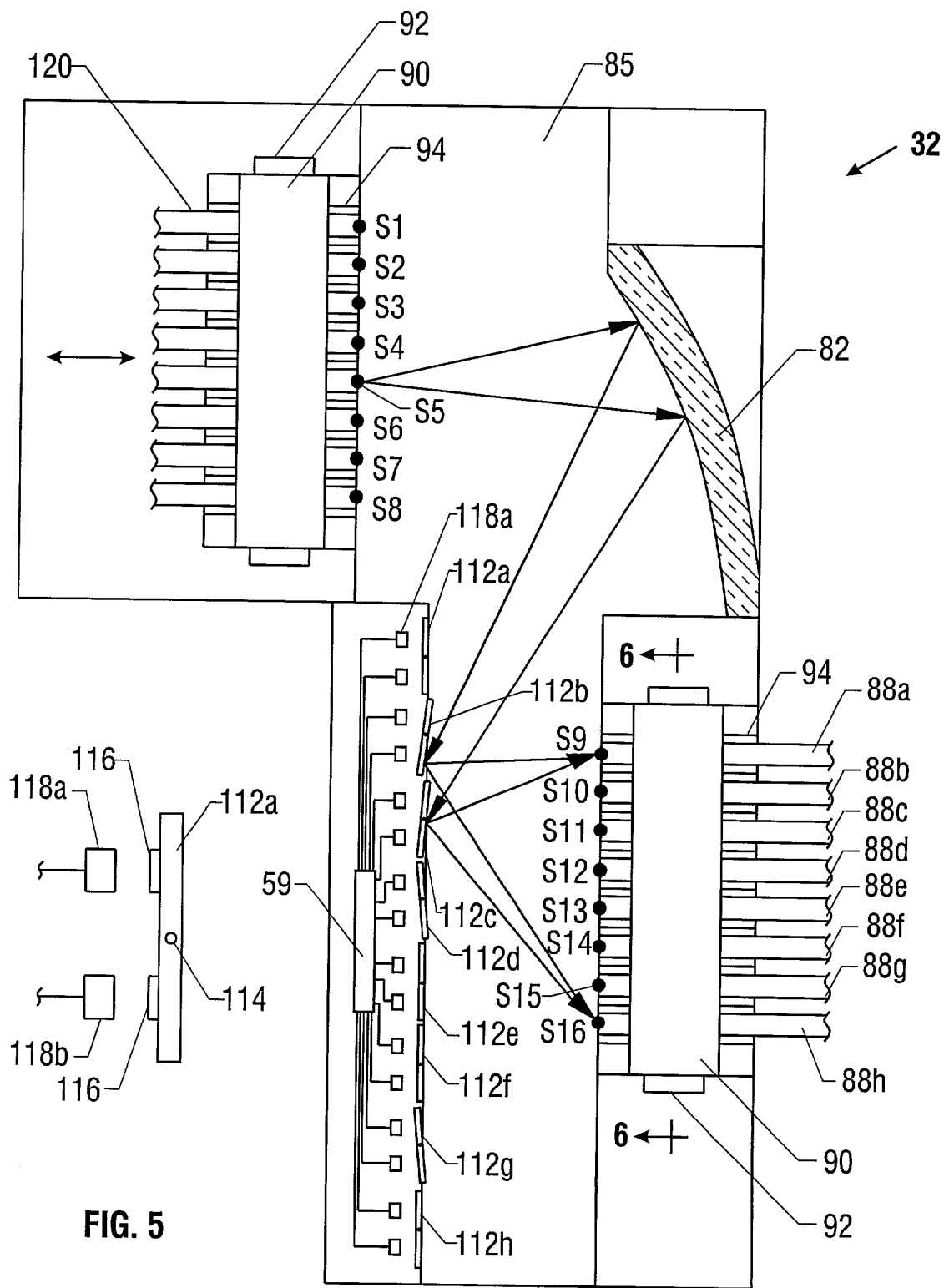


FIG. 4

FIG. 5

FIG. 6 is a cross-sectional view of the device 86, showing the device 86 in a cross-sectional view. The device 86 includes a substrate 85, a layer 88, a layer 90, a layer 92, a layer 94, a layer 96, and a layer 98. The device 86 is shown in a cross-sectional view, with the substrate 85 at the bottom, and the layers 88, 90, 92, 94, 96, and 98 stacked on top of each other. The layer 88 is a thin layer, and the layer 90 is a thicker layer. The layer 92 is a thin layer, and the layer 94 is a thicker layer. The layer 96 is a thin layer, and the layer 98 is a thicker layer. The device 86 is shown in a cross-sectional view, with the substrate 85 at the bottom, and the layers 88, 90, 92, 94, 96, and 98 stacked on top of each other.

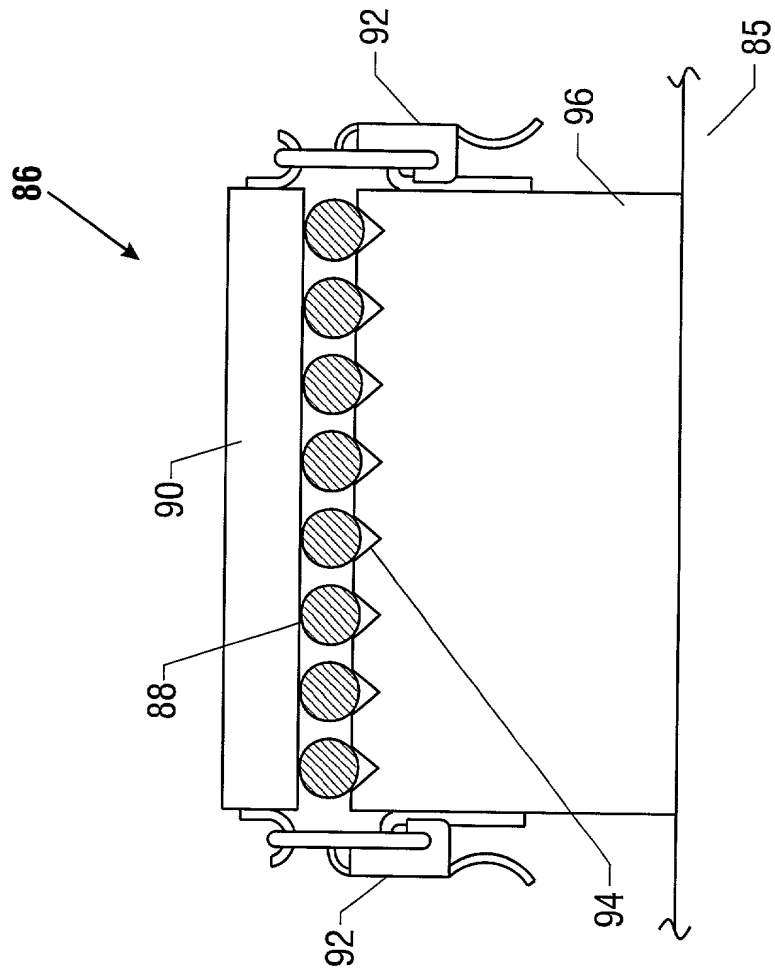


FIG. 6